

MANDATORY DISCLOSURE BY INSTITUTIONS RUNNING AICTE APPROVED ENGINEERING/TECHNOLOGY PROGRAMMES.

I. NAME OF THE INSTITUTION

- VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE
- DESHMUKHI VILLAGE
- POCHAMPALLY MANDAL
- NALGONDA DISTRICT
- A.P.
- PIN 508284
- PH. NO.08685- 226128/30/31/32/33/34/35/38/49
- FAX: 91-8685-226149

II.

NAME & ADDRESS OF THE PRINCIPAL

- DR. M. VENKATA RAMANA
- PRINCIPAL
- VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE
- DESHMUKHI VILLAGE
- POCHAMPALLY MANDAL
- NALGONDA DISTRICT
- A.P.
- PIN 508284
- PH. NO.08685- 226128
- FAX: 91-8685-226149

III. Name of the Affiliating University:

- JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY, HYDERABAD

IV. GOVERNANCE

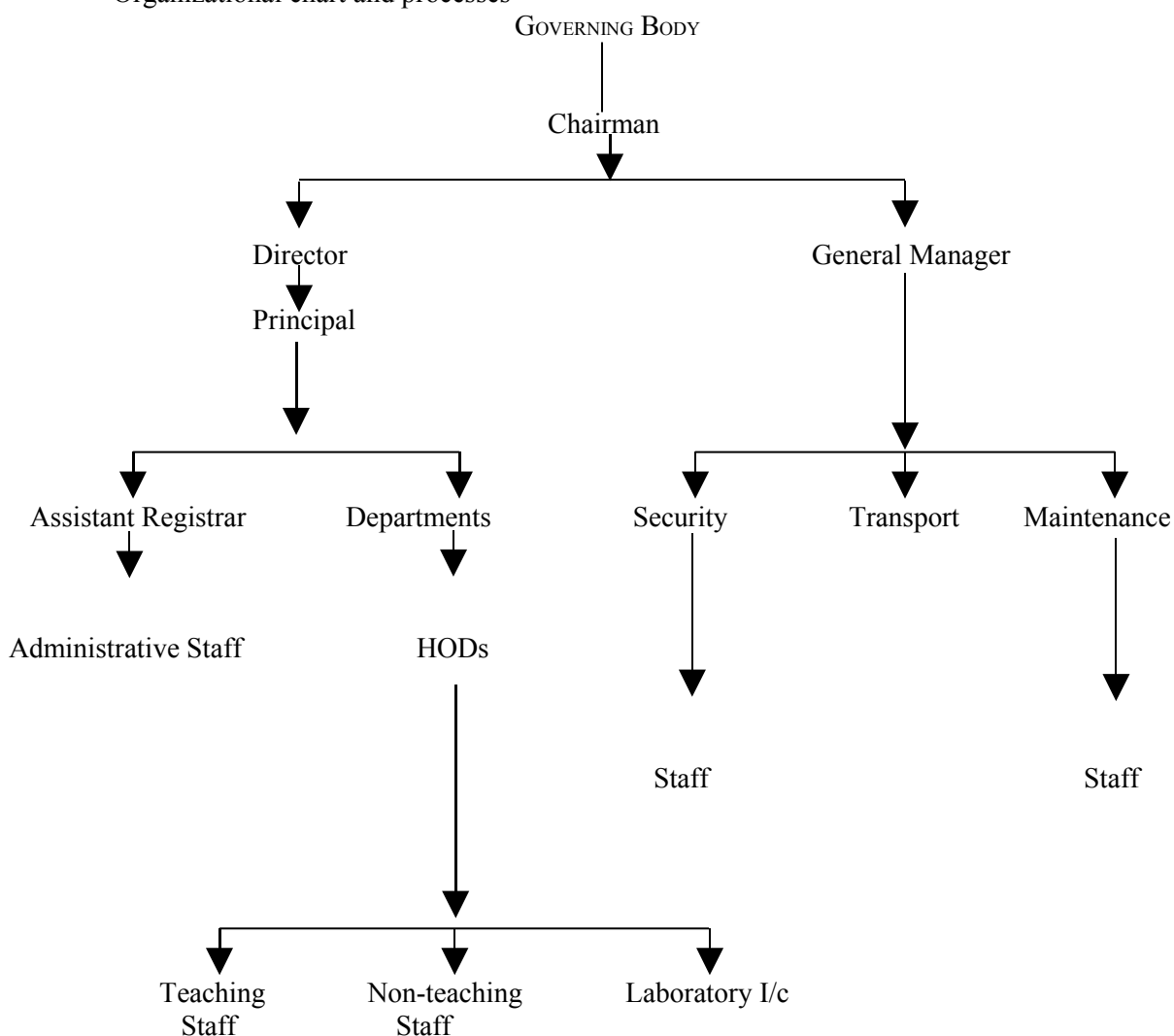
- ❖ Members of the Governing Board and their brief background

S.No	Name of the Member	Designation	Details
01	Dr. J. A. Chowdary	Chairman	Managing Director, M/S Portal Player Ltd., Hyderabad
02	Dr. L.Rathaiah	Member	President, Lavu Educational Society, Guntur.
03	Mr. A. Bapanna	Member	Managing Director, Kalajyothi Process Ltd. Hyderabad
04	Dr. S K Dubey	Member	Director, IIT Kharagpur
05	Mr. K. Prasanna Kumar	Member	Joint Managing Director, Embedded Infotech (P) Ltd, 8-2-684/J/7A, Road No. 12, Banjara Hills, Hyderabad
06	Mr. B. K. Bhadri	Member	Director, Southern Regional Office, AICTE, "Shastri Bhavan", 26, Haddows Road, Nungambakkam, Chennai - 600 006
07	Dr. A.V.S.S.K.S.Gupta	Member	JNTU College Of Engg., Hyderabad
08	Mr. SVUM Prasad	Member	Joint Director of DTE, Govt. of AP, B. R.K.R. Complex, Beside MCH Complex, Hyderabad
09	Prof. Jaya Prakasha Rao	Member	Vice Chairman, APSICHE, Govt. of AP, Opp. Mahavir Hospital, Mahavir Marg, Masab Tank, Hyderabad - 500 028
10	Prof. G. Ranga rao	Member Secretary	Director, VITS, Deshmukhi

Members of Academic Advisory Body

S.No	Name	Designation	Organization
01	Dr. L. Rathaiah	President	VITS
02	Dr. G.Tulasiram Das	Professor	JNTU College of Engg.
03	Mr. M.Hanumantha Rao	Dean (Academics)	NIT, Warangal
04	Mr.P.Suri Babu	Deputy General Manager	Corporate Recruitment Satyam Services Ltd.
05	Mr.P T Prabhakar	Regional Joint Director	DTE, Kakinada
06	Mr. Kedarnath Udiyavar	Sr. Vice President	Polaris Ltd.
07	Dr. K. Vizaya Kumar	Prof.& Head, Industrial Engg. and Management	IIT, Khargapur
08	Mr. M B Srinivas	Professor	IIIT, Hyderabad
09	Prof. G. Ranga rao	Director	VITS

- ❖ Frequency of the Board Meetings and Academic Advisory Body: **Once in a Year**
- ❖ Organizational chart and processes



- ❖ Nature and Extent of involvement of faculty and students in academic affairs/ improvements
 - Continuous counseling of students for improving their performance
 - Interaction with students by PRINCIPAL, HODs and Director to identify deficiencies in teaching – learning processes and attending to corrective action.

- ❖ Mechanism/Norms & Procedure for democratic/good Governance
 - The Institute is governed by a Governing Body (GB). The LAVU Educational Society (LES) shall nominate the Chairman of the GB and five members including the Director of the Institute. In addition, the AICTE, JNTU and Government of A.P. will nominate their representatives. The Governing Body will make all policy decisions regarding the courses to be offered, recruitment of staff, service conditions of staff, conduct of staff and students, other academic activities and non-academic activities.
 - VITS is an ISO – 9001: 2000 certified Institution and therefore it has developed quality management systems for excellence in both academic and administrative activities.

- ❖ Student Feedback on Institutional Governance/faculty performance
 - Student feedback is taken twice in a semester regularly on faculty performance and once in a semester on Institutional Governance. Also, feedback on Institutional Governance is taken through class counselors at regular intervals.

- ❖ Grievance redressal mechanism for faculty, staff and students
 - Suggestion boxes are provided for the students. Students, Parents and staff can also send their suggestions through E-mail to faculties PRINCIPAL / HOD / Director.

V. PROGRAMMES

- ❖ Name of the Programmes approved by the AICTE

UNDERGRADUATE COURSES:-

- B. Tech. in Mechanical Engineering
- B. Tech. in Electrical and Electronics Engineering
- B. Tech. in Electronics and Communication Engineering
- B. Tech. in Computer Science and Engineering
- B. Tech. in Electronics and Instrumentation Engineering

POSTGRADUATE COURSES

- *M. Tech. in Design and Manufacturing Engineering (Mechanical Engineering)*
- *MCA (Master of Computer Applications)*
- *MBA (Master of Business Administration)*
- *M.Tech. in VLSI Design (Electronics & Communication Engineering)*

- ❖ Name of the Programmes accredited by the AICTE: **NIL**

- ❖ For each Programme the following details are to be given:

UNDERGRADUATE COURSES:-

- | | |
|---|-----|
| ➤ B. Tech. in Mechanical Engineering | 60 |
| ➤ B. Tech. in Electrical and Electronics Engineering | 60 |
| ➤ B. Tech. in Electronics and Communication Engineering | 120 |
| ➤ B. Tech. in Computer Science and Engineering | 120 |
| ➤ B. Tech. in Electronics and Instrumentation Engineering | 60 |

- **Duration: All undergraduate courses are of four years duration**

POSTGRADUATE COURSES

- *M. Tech. in Design and Manufacturing Engineering (Mechanical Engineering)* --
18
- *MCA (Master of Computer Applications)* --
60
- *MBA (Master of Business Administration)* --
60
- *M.Tech in VLSI Design (Electronics & Communication Engineering)* --
18

- Cut off rank for admission during the last three years:
➤ The Details are given below.

S.NO.	BRANCH	2005		2006		2007		2008	
		LOW	HIGH	LOW	HIGH	LOW	HIGH	LOW	HIGH
01	CSE	6163	84181	5596	98365	6016	151332	8276	250996
02	ECE	4480	20187	5418	10203	4151	119324	5658	234669
03	EEE	9711	83651	8731	98954	9290	168627	9374	226847
04	MECH	11354	102217	12380	10290	11688	146312	10603	250996
05	EIE	27639	87756	21183	12471	22962	141162	22523	246674
06	MCA	--	--	--	--	--	---	6035	2101137
07	MBA	--	--	---	---	---	---	19715	179742
08	M.Tech.	--	---	---	---	---	---	564	1702

POST GRADUATE COURSE: -

- One PG Course in Mechanical Department with the title “Design and Manufacturing Engineering” and intake 18 has been granted by AICTE vide their letter no. F.No. 730-50-281(E)/ET/98 dated 21.09.2005 and is not started due to no admissions.
- Another PG Course in Computer Science Department with the Title of “Master of Computer Applications” and intake 60 has been granted by AICTE vide their letter no. F.No.730-50-281 (E)/ET/98 dated 30-07-07
- Another PG Course in MBA intake 60 has been granted by AICTE vide their letter no. F.No.730-50-281 (E)/ET/98 dated 31-07-08
- Another PG Course in M.Tech in VLSI Design intake 18 has been granted by AICTE vide their letter no. F.No.730-50-281 (E)/ET/98 dated 31-07-08

- **Placement Facilities:**

- Professor-in-charge Training and placement is appointed in the college. VITS has a city office at 503 Babu Khan Estate, Basheerbagh, Hyderabad- 500002, which looks after the training and placement activities. Soft Skills Development through Personality Development Programmes, Mock Interviews etc are conducted regularly.
- VITS has established an **Incubation center** in collaboration with M/S Embedded InfoTech Pvt Ltd at our city office and in which twelve of our CSE students are undergoing training during this academic year.
- Campus placement in the last three years indicating details regarding minimum salary, maximum salary and average salary are given below

Last three years placement

Year	No. Of Students Placed
2004	10
2005	13
2006	43
2007	191

Minimum Salary offered by companies: 1 lakh

Maximum Salary offered by companies: 3.5 lakh

Average Salary offered by companies: 1.75 lakh

- ❖ Name and duration of programme(s) having affiliation/collaboration with Foreign University(s)/Institution(s) and being run in the same Campus along with status of their AICTE approval. If there is foreign collaboration, give the following details: **NOT APPLICABLE**

Details of the Foreign Institution/University: **NOT APPLICABLE**

- ❖ For each Collaborative/affiliated Programme give the following: **NOT APPLICABLE**
- ❖ Whether the Collaborative Programme is approved by AICTE? If not whether the Domestic/Foreign Institution has applied to AICTE for approval as required under notification no. 37-3/Legal/2005 dated 16th May 2005. **NOT APPLICABLE**

VI. FACULTY

- ❖ Branch wise list of faculty members:
 - Permanent Faculty
 - Visiting Faculty
 - Adjunct Faculty
 - Guest Faculty
 - Permanent Faculty: Student Ratio
- } List is enclosed at **Annexure – 12(ii)(b)**
1:15
- ❖ Number of faculty employed and left during the last three years:
List is enclosed in **Annexure – (VI)**

VII. PROFILE OF DIRECTOR/PRINCIPAL WITH QUALIFICATIONS, TOTAL EXPERIENCE, AGE AND DURATION OF EMPLOYMENT AT THE INSTITUTE CONCERNED

Principal name : **Dr. M. Venkata Ramana**

Qualifications : **M.Tech., Ph.D.**

Experience : **20**

Date of Joining VITS : **09-02-2009**

For each Faculty give a page covering the Profile: List enclosed in **Annexure – 2A(i)**

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VIII. FEE

- ❖ Details of fee, as approved by State fee Committee, for the Institution.
- ❖ Time schedule for payment of fee for the entire programme.

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Fee Details :

UG-COURSE-B.TECH.

S.No.	Title of Fee	Frequency (Monthly/per Semester/ Year/Entire Course)	Amount (Rs.)
1	Admission Fee	Entire Course	5000-00
2	JNTU Infrastructure Fee	Entire Course	2500-00
3	Tuition Fee Per year	Per Year	30200-00
4	NRI Fee	Per Year	91700-00

PG COURSE MCA & MBA

S.No.	Title of Fee	Frequency (Monthly/per Semester/ Year/Entire Course)	Amount (Rs.)
1	Admission Fee	Entire Course	5000-00
2	JNTU Infrastructure Fee	Entire Course	2500-00
3	Tuition Fee Per year	Per Year	26700-00
4	NRI Fee	Per Year	73100-00

IV.

PG COURSE M.TECH.

S.No.	Title of Fee	Frequency (Monthly/per Semester/ Year/Entire Course)	Amount (Rs.)
1	Admission Fee	Entire Course	5000-00
2	JNTU Infrastructure Fee	Entire Course	2500-00
3	Tuition Fee Per year	Per Year	55000-00
4	NRI Fee	Per Year	110000-00

❖ Estimated cost of boarding and Lodging in Hostels. **NA**

Ix. ADMISSION

❖ Number of seats sanctioned during the year of approval.
576, as per AICTE Approval letter No.F.No.730 – 50 – 281 (E) / ET / 98, Dt.31.07.2008

Number of students admitted under various categories each year in the last three years:

➤ **Details are given below**

2006 – 2007

Branch	OC	BC	SC	ST	CAP	NCC	SP	TOTAL
EEE	28	15	1	--	--	1	--	46
MECH	31	19	3	--	--	--	--	53
ECE	67	33	14	5	--	--	--	119
CSE	32	18	08	03	--	--	--	61
EIE	39	12	--	--	--	--	--	51

2007 – 2008

Branch	OC	BC	SC	ST	CAP	NCC	SP	TOTAL
EEE	33	15	03	02	--	--	--	53
MECH	33	16	01	01	--	--	--	51
ECE	49	35	13	05	2	1	--	105
CSE	59	29	13	06	--	--	--	107
EIE	29	18	--	--	--	--	--	47

2008 – 2009

Branch	OC	BC	SC	ST	CAP	NCC	SP	TOTAL
EEE	18	20	07	02--	--	--	--	47
MECH	19	19	10	00	--	--	--	48
ECE	47	35	12	05	02	--	01	106
CSE	49	38	13	04	--	--	--	94
EIE	23	18	04	01	--	--	--	46
MCA	17	20	06	01	--	--	--	44
MBA	15	21	06	03	--	--	--	45
M.Tech.	--	02	05	03	--	--	--	10

- ❖ Number of applications received during last two years for admission under Management Quota and number admitted.

➤ Admissions under Management quota are:

Category	2005	2006	2007	2008
Management	25	45	72	100

X. ADMISSION PROCEDURE

- ❖ Mention the admission test being followed, name and address of the Test Agency and its URL (website).
 - The admissions to the institute are made as per rules/regulations framed by the state government/AICTE. The state test agency is Department of Technical Education through EAMCET. The college website is <http://www.vignanits.ac.in>
- ❖ Number of seats allotted to different Test Qualified candidates separately [AIEEE/CET (State conducted test/University tests)/Association conducted test]
 - The seats are as per the quota and norms set by JNTU, Hyderabad, to which VITS is affiliated.
- ❖ Calendar for admission against management/vacant seats:
 - Last date for request for applications.
 - Last date for submission of application.
 - Dates for announcing final results.
 - Release of admission list (main list and waiting list should be announced on the same day)
 - Date for acceptance by the candidate (time given should in no case be less than 15 days)
 - Last date for closing of admission.
 - Starting of the Academic session.
 - The waiting list should be activated only on the expiry of date of main list.
 - The policy of refund of the fee, in case of withdrawal, should be clearly notified.
 - Management decides on dates for management quota admissions after JNTU announcement. Accordingly, the society gives a paper advertisement. Against the paper advertisement on the scheduled dates candidates are interviewed and seats are allotted.

XI. CRITERIA AND WEIGHTAGES FOR ADMISSION

- ❖ Describe each criteria with its respective weightages i.e. Admission Test, marks in qualifying examination etc.
- ❖ Mention the minimum level of acceptance, if any.
- ❖ Mention the cut-off levels of percentage & percentile scores of the candidates in the admission test for the last three years.

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- ❖ Display marks scored in Test etc. and in aggregate for all candidates who were admitted.
 - These are centrally counseled through EAMCET convener, Department of Technical Education, Government of Andhra Pradesh.

V. APPLICATION FORM

- ❖ Downloadable application form, with online submission possibilities.

VI. LIST OF APPLICANTS

- ❖ List of candidates whose applications have been received along with percentile/percentage score for each of the qualifying examination in separate categories for open seats. List of candidates who have applied along with percentage and percentile score for Management quota seats.

VII. RESULTS OF ADMISSION UNDER MANAGEMENT SEATS/VACANT SEATS

- ❖ Composition of selection team for admission under Management Quota with the brief profiles of members (This information be made available in the public domain after the admission process is over)
- ❖ Score of the individual candidates admitted arranged in order of merit.
- ❖ List of candidates who have been offered admission.
- ❖ Waiting list of the candidates in order of merit to be operative from the last date of joining of the first list candidates.
- ❖ List of the candidates who joined within the date, vacancy position in each category before operation of waiting list.
 - The details with regard to item no XII to XIV are being incorporated in the website for dynamic updating.

VIII. INFORMATION ON INFRASTRUCTURE AND OTHER RESOURCES AVAILABLE

LIBRARY:

- Number of Library books/Titles/Journals available (programme-wise)

Details of the Library facilities:

DEPARTMENT	No.of.Titles	No.of.Volumes	Journals	
			National	International
EEE	698	2255	2	ASME, IEEE, ACM, MMT journals are subscribed online through SONET & DELNET
ME	754	2556	7	
ICE & ECE & EIE	852	3319	2	
CSE	1000	3511	6	
MCA	160	1120	-	
Basic Sciences	956	3685	26	
General	677	694	--	
MBA	160	1150	--	
M.Tech.	75	450	--	
SC & ST Integrated Book Bank	233	786	-	

- Commerce server
- Exchange server
- Exchange conferencing server
- SQL server enterprise edition
- Systems Management server
- Share point portal server
- Content management server
- Application center server
- Internet security & acceleration server
- Visual Studio .NET
- Visio Professional
- SDKs and DDKs
- Microsoft Project Professional 2002, Project Server 2002
- Macro assembler 6.11

As a part of Academic Alliance membership the department has access to MSDN library, Tech Support newsgroup, Tech Support Library, Betas, new releases and updates, MSN website with resource for faculty.

Other than the above the Institute has Rational Rose Enterprise Edition software, Pspice software, VLSI software, IRONCAD software packages are available. We also use quite a good number of open source operating systems, application software packages that are freely available on net.

- Special purpose facilities available

The Institute has multi function printer, scanners, web cam, digital camera etc.

WORKSHOP:

- List of facilities available.

Carpentry& Pattern Making:

- Cutting practice and chiseling
- Tenon and mortise joint
- Single piece pattern

FITTING

- Square filing,
- Dovetail joint,
- Half round joint.

TIN SMITHY

- Cutting Practice,
- Cylinder
- Funnel
- Dust pan.

FOUNDRY

- Single piece pattern,
- Split pattern,
- Sweep pattern

BLACK SMITHY

- Round to square,
- Square to round,
- Hook

HOUSE WIRING

- One way switch,
- Two way switch,
- Series of lamps,

- Tube light connection

Games and Sports Facilities: Details are enclosed in **Annexure – XV (b)**

Extra Curriculum Activities:

- The students are encouraged to participate in various college events, Inter college competitions, Student Paper Presentation in National Seminar Symposiums etc.
- The detailed activities wherein VITS students have excelled is briefed below:
Institute aggressively pursues the CCA/ECA activities. A total no of 101 students from various departments have participated in different events conducted by local and National institutes.

The participation details of CCA & ECA activities are given below:

S.No	Item	CCA	ECA	Total
1	Number of Students Participated	80	21	101
2.	Events	31	2	33
3.	Prizes	20	1	21

Many of the students from VITS have won awards /prizes. One of the awards was at IIT Chennai, the details of which are:

IIT Chennai, conducted a National level technical event, an ISO Certified event, called SHAASTRA 2005 (Rs. 2.5 crores event) from 5th to 10th October 2005, at Chennai. 1500 students from all over the country participated. **Students of VITS (ECE & ICE students) bagged the 1st prize for their presentation on Discrete Circuit Design.**

List of Events where Prizes won

S.No.	Name & No.	Event & Place	Date of participation
1	1) S. Rithika III/IV - ECE	A National Level Paper Presentation at Mahatma Gandhi Institute of Technology.	23/02/07 & 24/02/07
	2) P. Raghuvharun Reddy III/IV -EEE.		
2	1) V. Kurmila III/IV - EEE	A National Level Paper Presentation at Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada	23/02/07 & 24/02/07
	2) V. Vishnu Kumar III/IV - EEE		
3	1) B. Brinda IV - EEE	A National Level Paper Presentation at Scient Institute of Technology & Science, Ibrahimpatnam	24-02-07
	2) Soumitri Pavan IV - EEE		
	3) M. Vasunder II/IV - EEE		
	4) D. Krishna Teja II/IV - EEE		
4	P. Raghu Varun Reddy III/IV - EEE	A National Level Paper Presentation at Chaitanya Bharathi Institute of Technology, Gandipet	26-02-07 & 27-02-07
5	V. Swaroop Kumar II/IV - EEE	A National Level Paper Presentation at Guru Nanak Engineering College, Ibrahimpatnam	17-03-07
6	M. Sundeep Chowdary III/IV - EEE	A National Level Paper Presentation at J N T U	09-03-07
7	1) V. Pradeep IV - ECE	A National Level Paper Presentation at KITS, Warangal	24-02-07
	2) B. Vinay Kumar IV - ECE		
8	K. Vaishnavi	A National Level Paper Presentation at Scient Institute of Technolgy & Science, Poster Presentation	24-02-07
9	1) Tanuja I/IV - EEE	A National Level Paper Presentation at Dumbsherads, VEC Guntur.	29-12-06 to 30-12-06
10	1) M. Vasunder II/IV - EEE	A National Level Paper Presentation at S.I.T.S Ibrahimpatnam	23-02-07 to 24-02-07
	2) D. Krishna Teja II/IV - EEE		
	3) K.S. Mahathee II/IV- EEE		
	4) K. Vaishnavi II/IV - EEE		
11	1) S. Manasa II/IV - EEE	A National Level Paper Presentation at Bharat College of Engineering Ibrahimpatnam	05-03-07
	2) P. Sushma II/IV - EEE		
12	1) V. Mahesh II/IV - EEE	A National Level Paper Presentation at K.I.T.S Warangal	08-03-07
	2) M. Kamesh II/IV - EEE		
	3) D. Vijay Darshan Reddy II/IV - EEE		
	4) M.D. Nagoor Hasan		
13	1) N. Rajasekhar II / IV - EEE	A National Level Paper Presentation at - Bharat College of Engineering & Technology, Ibrahimpatnam	08-03-07

14	Ezra Raj III/IV - EEE	A National Level Paper Presentation at Paper presentation - G.N.I.T.S, Hyd.	
15	C. Nikhil Reddy III/IV - EEE		
16	Ch. Naga Rani III/ IV -EEE	Project Presentation V.R. Siddhartha Engineering College, Vijayawada.	23-02-07 & 24-02-07
17	K. Sandeep III/IV - EEE	Paper presentation G.N.I.T.S, Hyderabad	
18	M. Sandeep III/IV - EEE	Paper Presentation, J.N.T.U, Hyderabad.	09-03-07 & 10-03-07
19	P. Sri Harsha III/IV - EEE	Paper Presentation, V.R. Siddhartha Engineering College, Vijayawada.	23-02-07 & 24-02-07
20	1) S. Ravi Shankar III/IV - EEE 2) Y. Naveen Reddy III/IV – EEE	Paper presentation Bharat College of Engineering & Technology, Ibrahimpatnam	05-03-07
21	G. Praveen Kumar Reddy III/IV – EEE P. Raghu Varun Reddy III/IV - EEE	Paper presentation -G.N.I.T.S, Hyderabad	
22	1) Charan Jasti III/IV – EEE	Paper presentation V.R. Siddhartha Engineering College, Vijayawada	23-02-07 & 24-02-07
	2) V. Sudha III/IV – EEE		
	3) V. Vishnu Kumar III/IV – EEE		
23	1) Y. Swetha III/IV – EEE	Project Presentation, V.R. Siddhartha Engineering College, Vijayawada.	23-02-07 & 24-02-07
	2) G.Sravanthi III/IV – EEE		
	3) V. Kurmila III/IV – EEE		
	4) M. Sravanthi III/IV - EEE		
24	1) S. Pavan IV – EEE	Paper presentation Scient Institute of Technology & Science, Ibrahimpatnam	23-02-07 & 24-02-07
	2) Brinda IV – EEE		
	3) Krishna Teja II/IV – EEE		
	4) Vasunder II/IV - EEE		
	5) K.S. Mahati II/IV – EEE		
	6) Vaishnavi II/IV - EEE		
25	1) Vishnu Kumar III/IV- EEE	Model Project Exhibition Contest V.R. Siddhartha Institute of Technology & Science, Vijayawada	23-02-07 & 24-02-07
	2) Kurmila III/IV – EEE		
26	1. C.Avinash	Quiz Competition	NIT Warangal
	2. M.Harish Nandan		
	3. T.Shashidhar		

Soft Skill Development Facilities:

- VITS conducts periodically Personality Development Programmes, Mock Interview, Group Discussions, and Analytical Tests etc, to develop Soft Skills in the Students.
- Number of seminar halls and size of each : 2 Nos. of 400 Sq.mts. each
- Number of Classrooms and size of each : 28 Nos. of 120 sq. mts. each =3360
- Number of Tutorial rooms and size of each : 15 Nos. of 80 sq. mts each. Total =1800 Sqm.
- Number of laboratories and size of each : 30 Nos. of 250 sq. mts each.
- Number of drawing halls and size of each : 02 No. 180 sq. mts.
- Number of Computer Centres with capacity : 03 Nos. of 240 sq.mts. Each.
- Central Examination Facility, Number of rooms and capacity of each: 01 No. 175 sq. mts

Teaching Learning process :

- Curricula and syllabi for each of the programmes as approved by the University.
 - Academic Calendar of the University
 - Academic Time Table
 - Teaching Load of each Faculty
 - Internal Continuous Evaluation System and place
- } As per
JNTU
Norms

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- Students' assessment of Faculty, System in place. Through Feed Back

For each Post Graduate programme give the following:

- Brief profile of each faculty : **Enclosed Annexure 2**
- Laboratory facilities exclusive to the PG programme:

Laboratory & Software facilities for MCA & MBA

Subject	Type of software & Versions	No of systems
UNIX	Sco Unix 5.0 Red hat 9	65
D.B.M.S.	Oracle 9i	65
Object oriented language (cpp)	Tueboc3	65
Computer Organizations	MASM	65

1. English Language Communications Lab
2. IT Lab
3. Object oriented language
4. Computer organization
5. Unix
6. D.B.M.S

Configuration:

1. P4 System
2. Intel G31 Mother Board
3. Core to duo 2.6
4. 1GB Ram
5. 160 GB Hard Disk
6. 15 in techs L.C.D.

M.Tech.:

Laboratories:

1. HDL Programming & EDA Tools Lab
2. Mixed signal Lab.

Equipments: 25 Computer System with software's

- i. Xilinx ISE 8.1 c
- ii. Modelsim XE III 6.0d
- iii. ORCAD 9.0

List of Experiments:

1. HDL Programming & EDA Tools Lab Experiments:

1. Digital circuits Description using verilog and VHDL
2. Verification of the functionality of Designed circuits using function simulator.
3. Timing simulation for critical path time calculation
4. Synthesis of Digital circuits.
5. Place and Route techniques for major FPGA vendors such as XILINX, Altera and Actes etc.
6. Implementation of Designed Digital circuits using FPGA and CPLD devices.

2. MIXED SIGNAL LABORATORY:

1. Analog circuits simulation using spice.
2. Mixed signal simulation using mixed signal simulators.
3. Lay out extraction for Analog & mixed signal circuits.
4. Parasitic values estimation from layout
5. Lay out VS schematic
6. Net list extraction.
7. Design Rule checks.